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CofC

Applicant: R. Goues et al.

Patent No.: 7,531,379

Issue Date: May 12, 2009

Title: METHOD OF FORMING CMOS IMAGER PIXELS WITH CAPACITOR STRUCTURES

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

We are transmitting herewith the attached:

- Request for Certificate of Correction.
- Certificate of Correction Form - PTO-1050 (in duplicate)
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Customer Number: 26809

Certificate

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of Correction

Respectfully submitted,

//Richard Henkler//

By: _____

Rich Henkler

Reg. No. 39,220

Date: June 17, 2009

Jenny Mundt

Name

Signature

IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,531,379

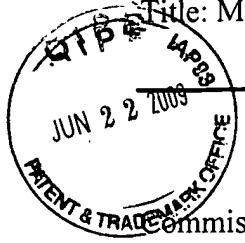
Docket No: 2001-1118.01/US

Issue Date: May 12, 2009

Patentee: Rhodes et al.

Title: METHOD OF FORMING CMOS IMAGER PIXELS WITH CAPACITOR
STRUCTURES

REQUEST FOR CERTIFICATION OF CORRECTION


Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

//Richard Henkler//

Date: June 17, 2009

By: _____

Rich Henkler
Reg. No. 39,220

CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 17 day of June 2009

Jenny Mundt

Name



Signature

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,531,379

Page (1) of 1

DATED : May 12, 2009

INVENTOR(S) : Rhodes et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (54), in "Title", in column 1, line 1, after "IMAGER" insert - - PIXELS - -.

On the face page, in field (75), in "Inventors", in column 1, line 2, delete "Moridian," and insert - - Meridian, - -, therefor.

In column 1, line 1, after "IMAGER" insert - - PIXELS - -.

In column 13, line 17, in Claim 31, delete ""contact" and insert - - contact - -, therefor.

In column 13, line 19, in Claim 31, delete "transistor." and insert - - transistor. - -, therefor.

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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